



2185

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Xiaolin Wang, et al

Serial No: 09/941,144

Group Art Unit: 2185

Filed: August 28, 2001

For: Method of Scalable Non-Blocking Shared Memory Output-Buffered Switching
Of Variable Length Data Packets From Pluralities Of Ports At Full Line Rate,
And Apparatus Therefor

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INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231

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Sir:

Attention is invited to the prior art identified in the application, the relevance of
which is there discussed.

Respectfully submitted,

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